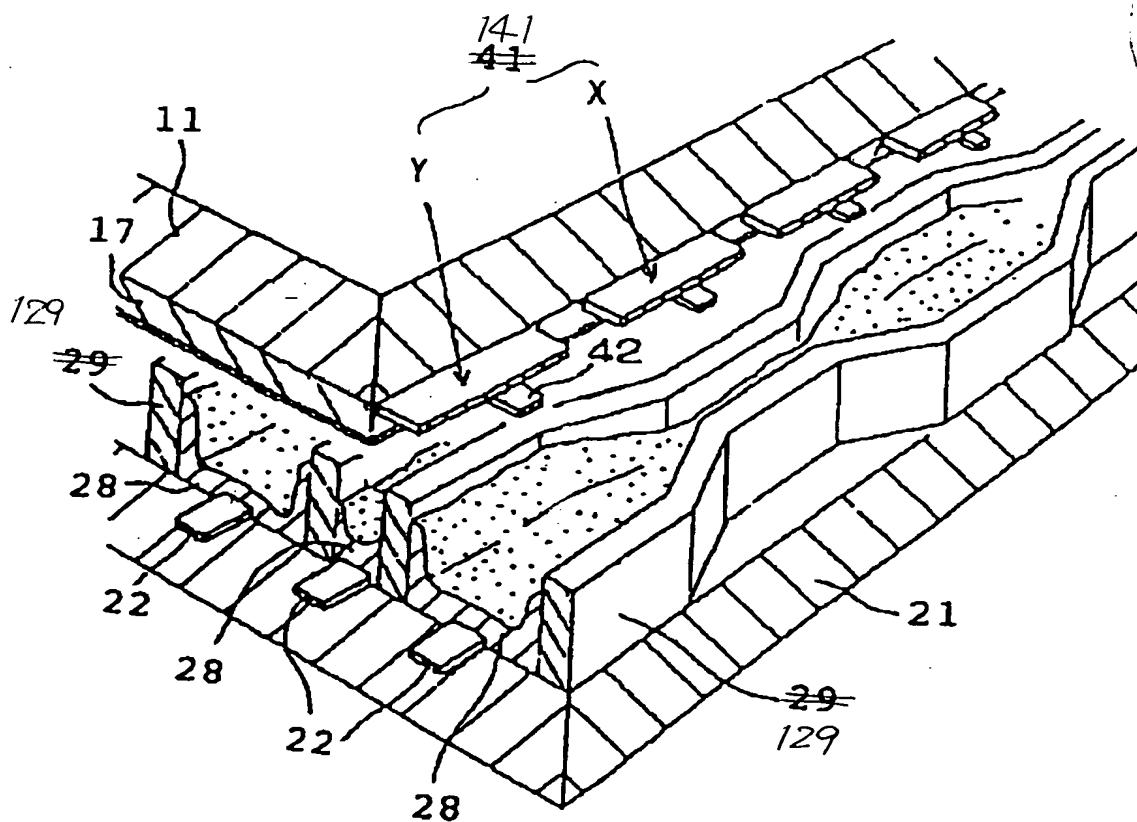


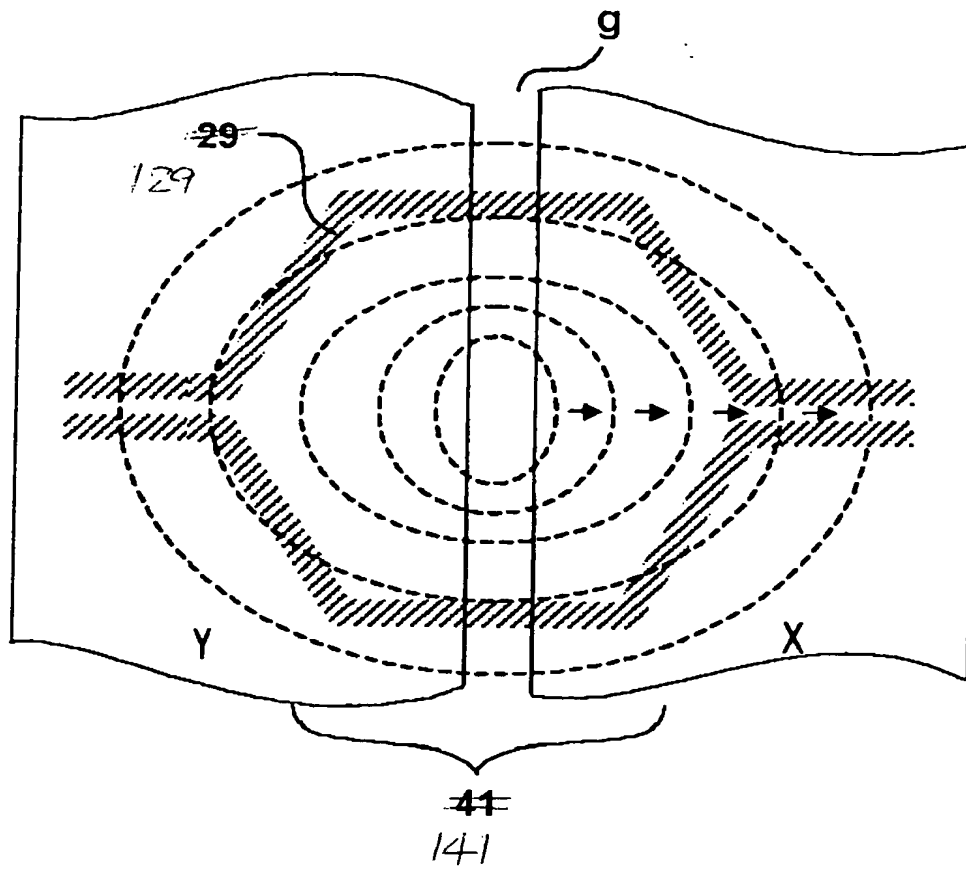
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Fig. 1 is a perspective view of a first embodiment of a semiconductor device. The device consists of a substrate 11 with a grid of square openings. Below the substrate is a layer 17 with a grid of rectangular openings. At the bottom of the device are two rows of circular elements 28, each connected to a horizontal bar 22 by a vertical post 29. The entire assembly is mounted on a base 21. Coordinate axes X and Y are shown.

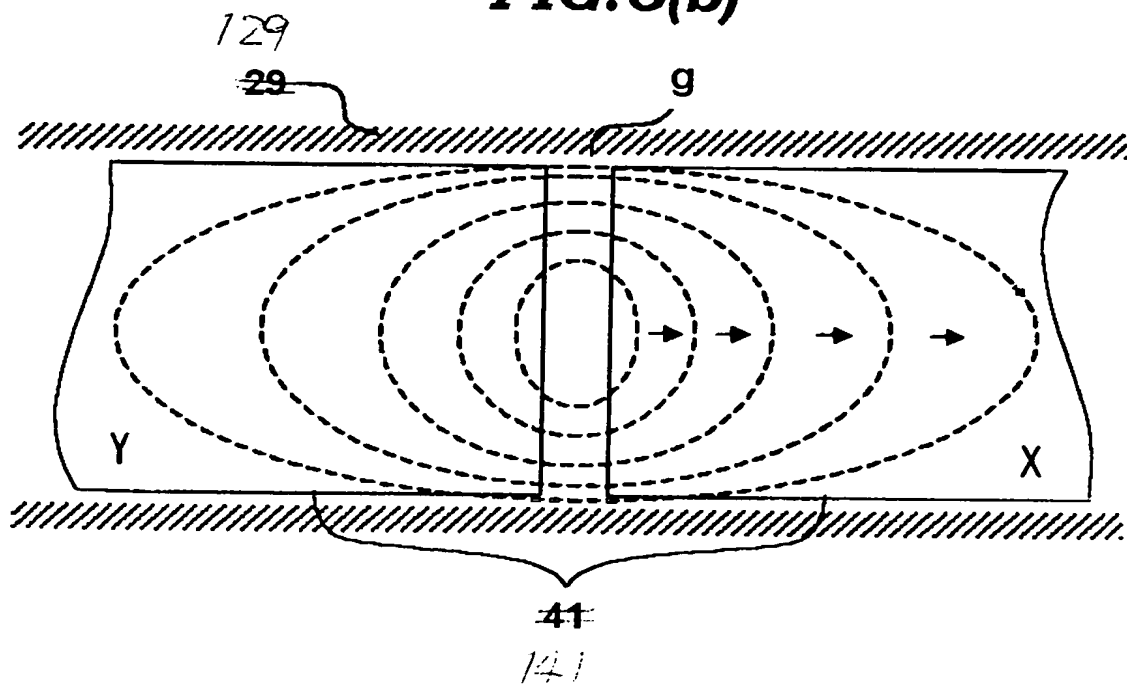
**FIG. 2**  
**PRIOR ART**



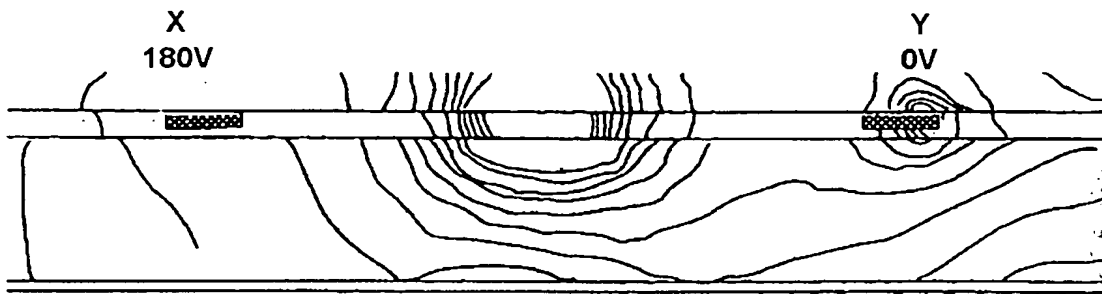
**FIG. 3(a)**



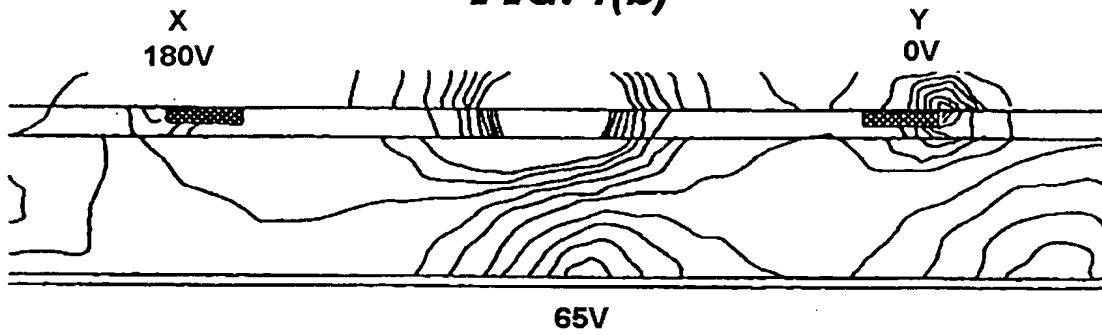
**FIG. 3(b)**



**FIG. 4(a)**

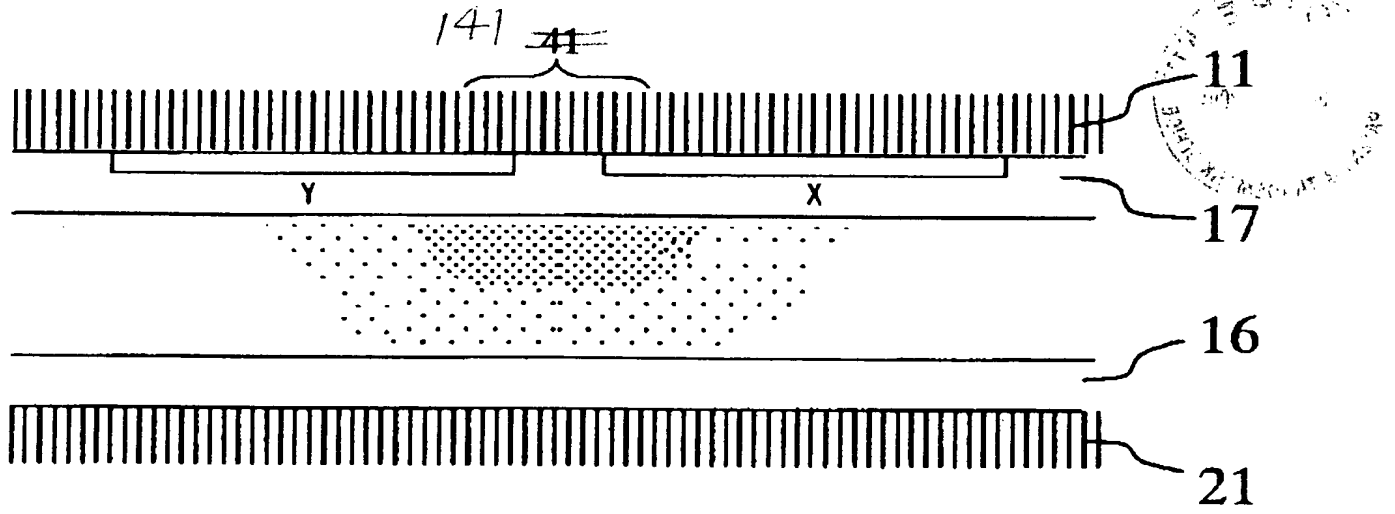


**FIG. 4(b)**

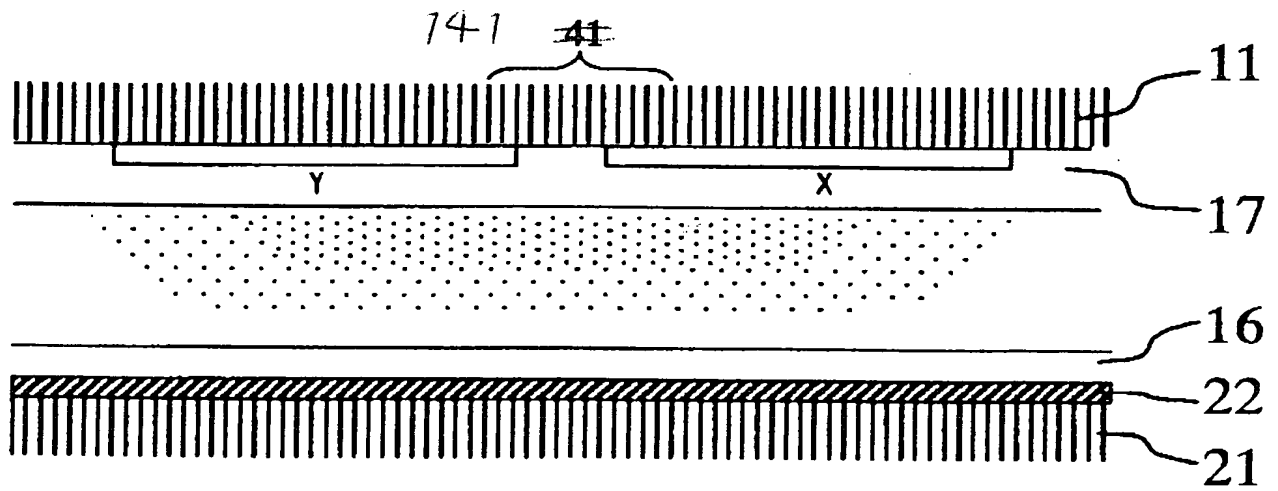


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**FIG. 5(a)**

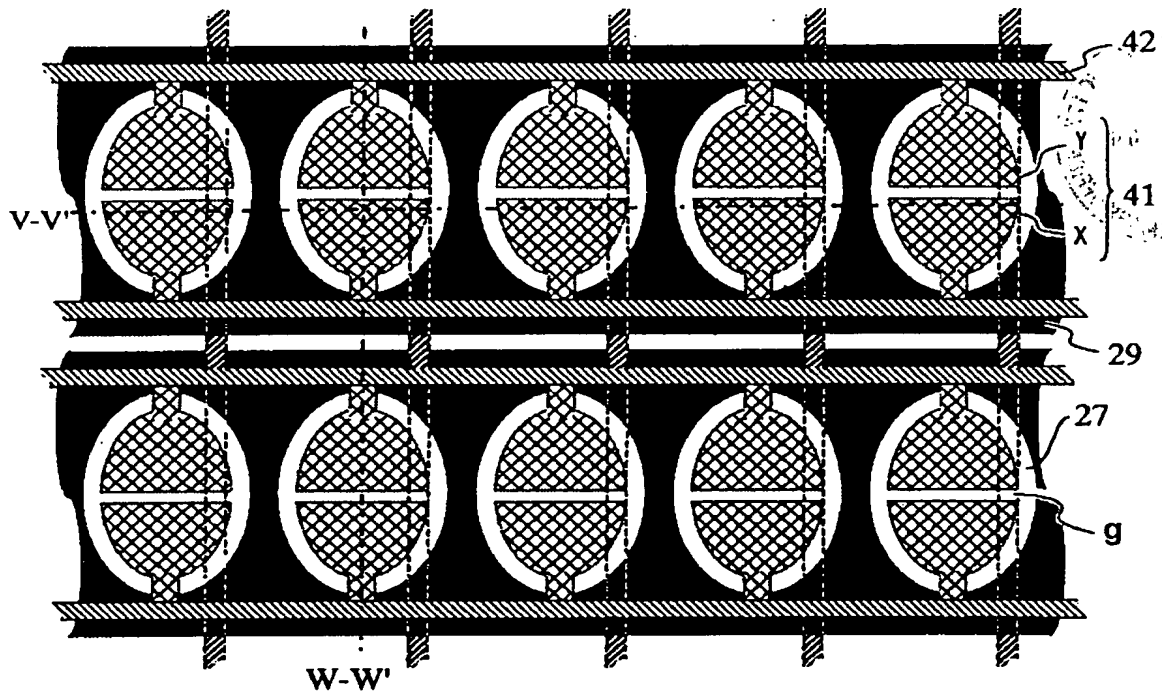


**FIG. 5(b)**

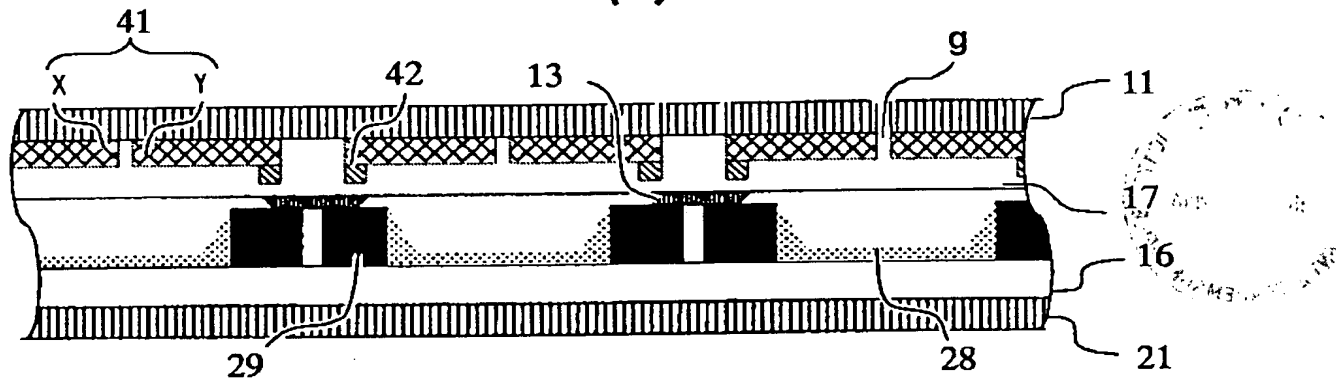


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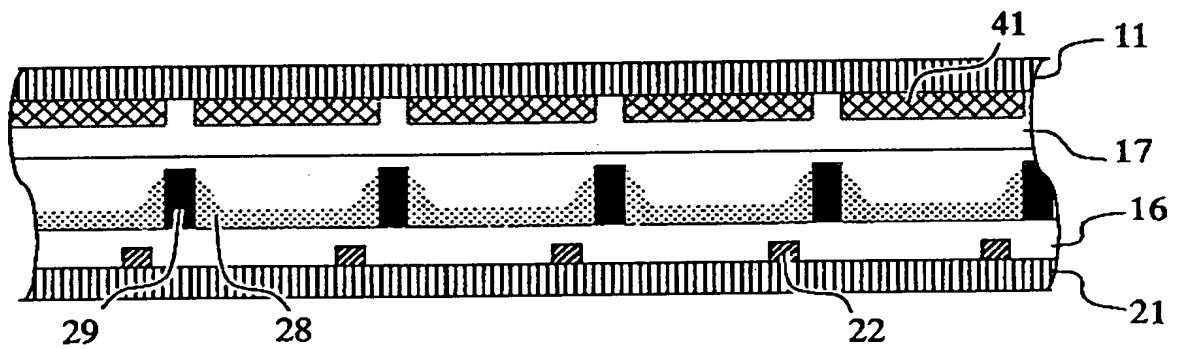
FIG. 6(a)



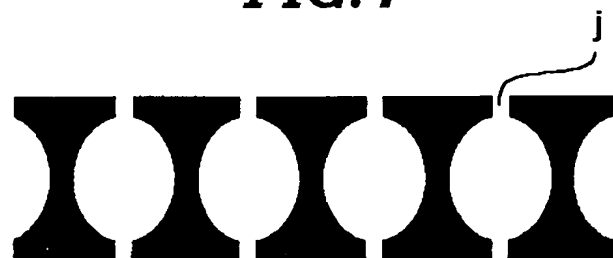
**FIG. 6(b)**



**FIG. 6(c)**



**FIG. 7**

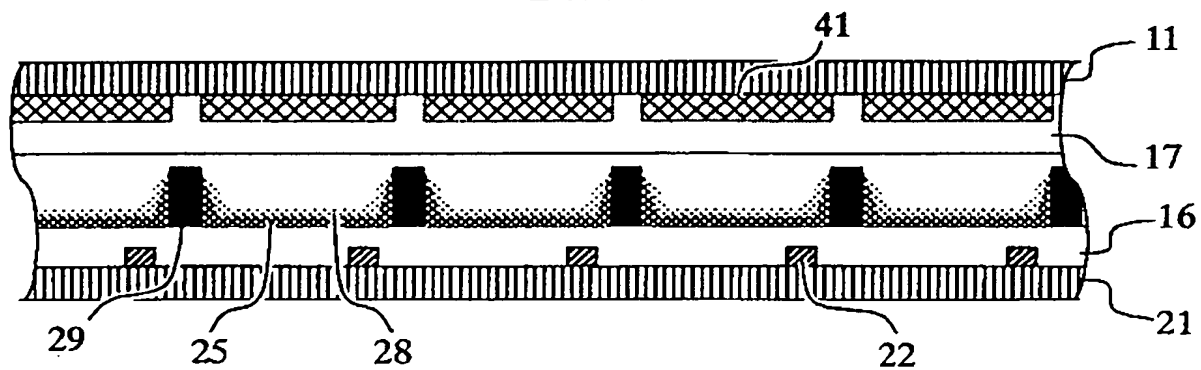


**(a)**



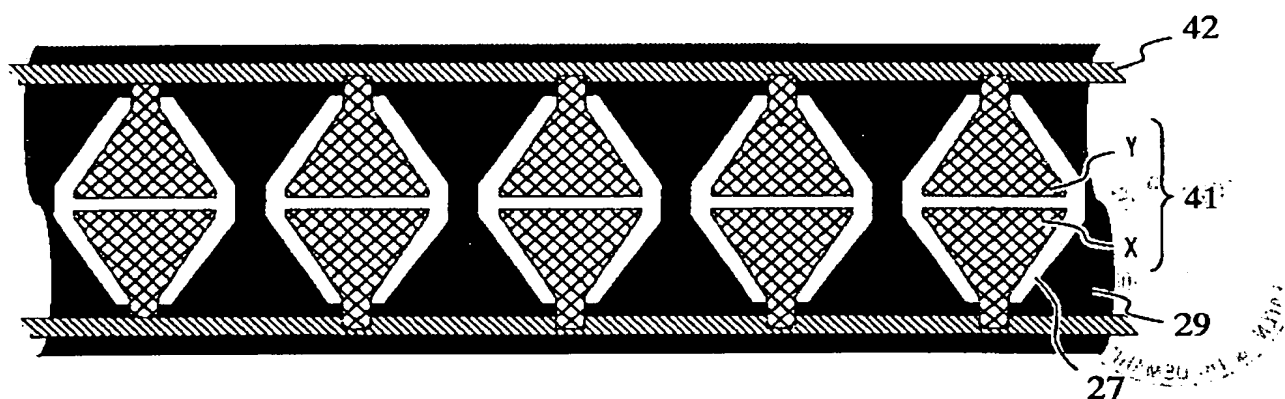
**(b)**

**FIG. 8**

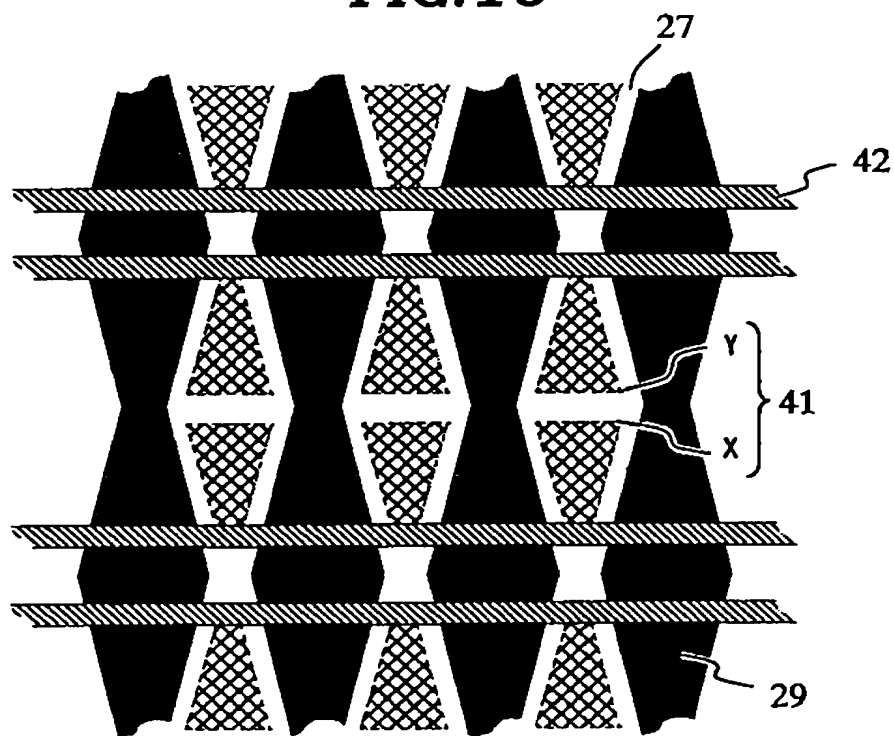




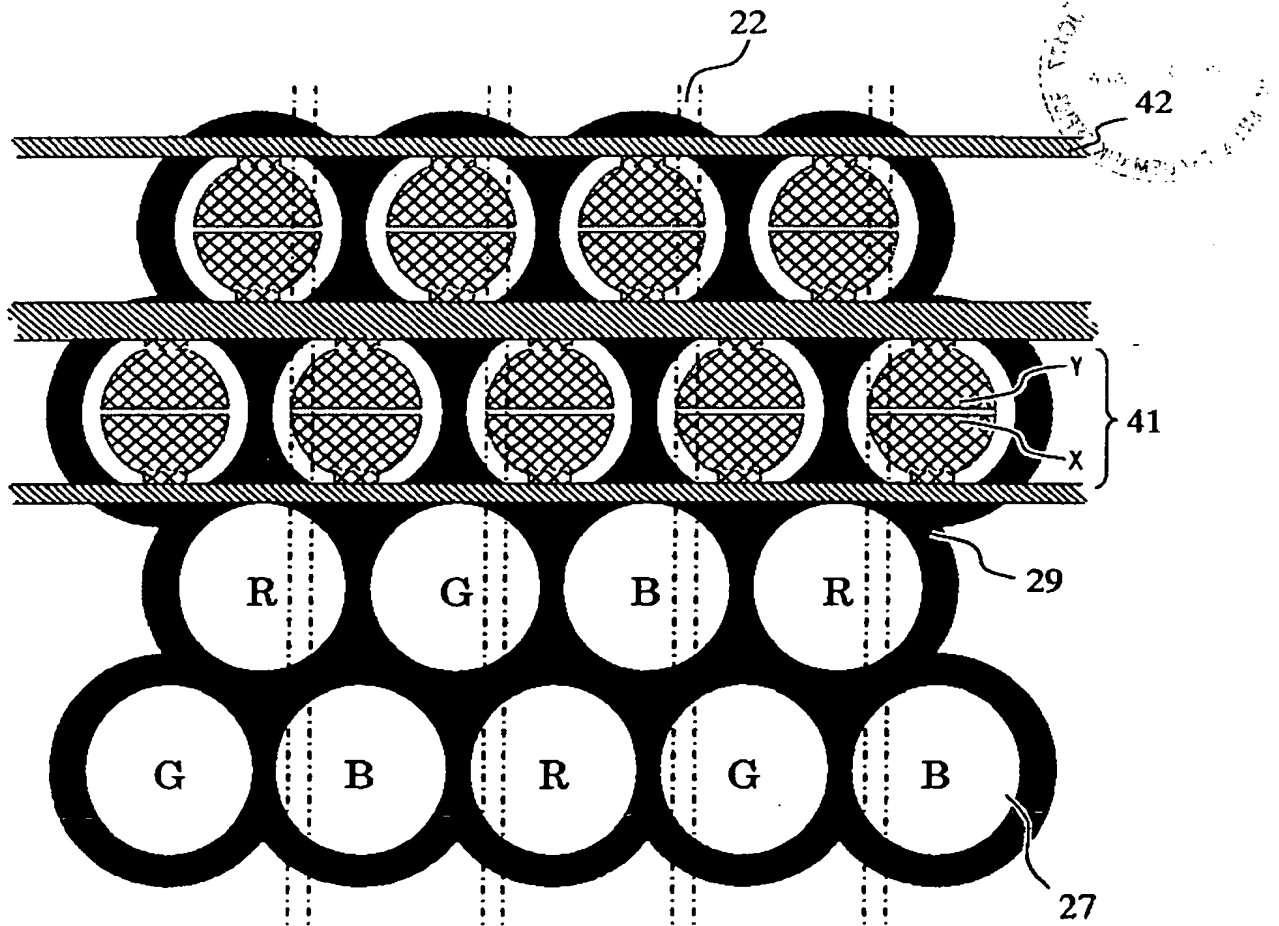
**FIG. 9**



**FIG. 10**

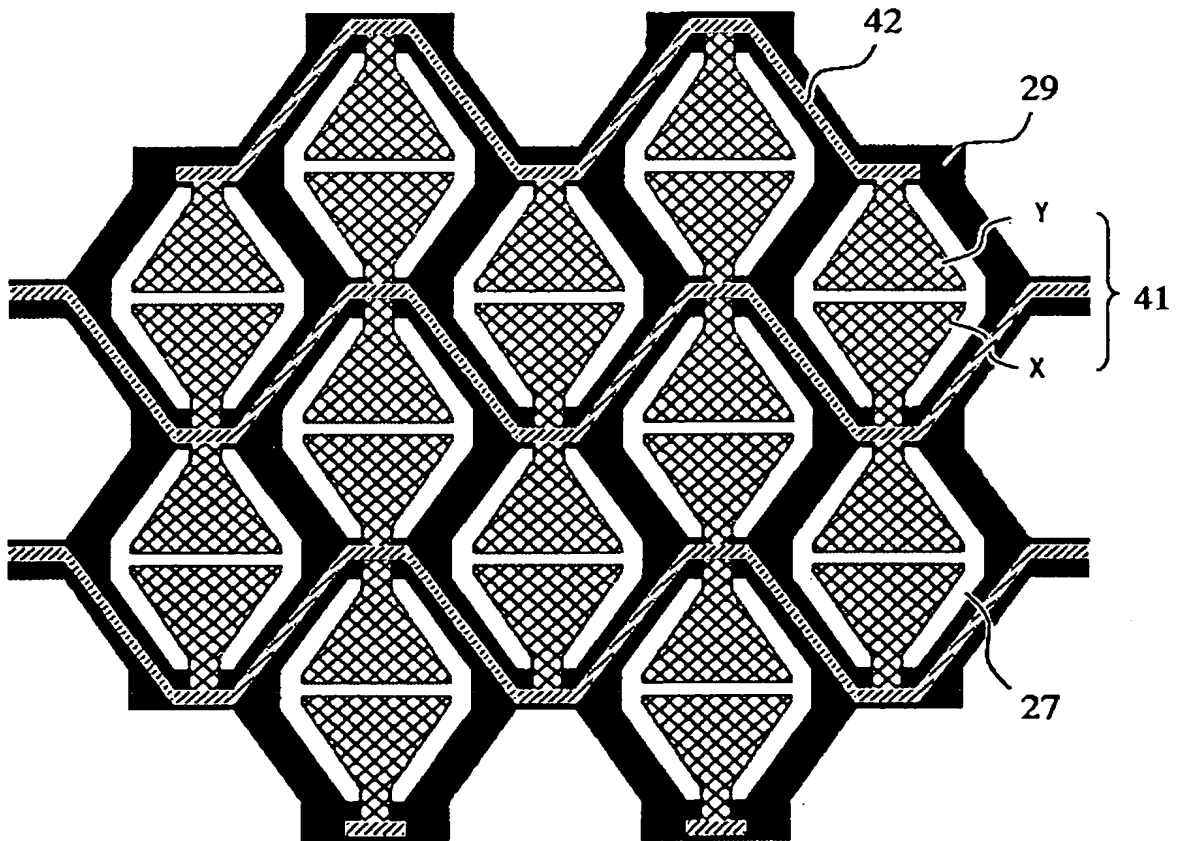


**FIG. 11**



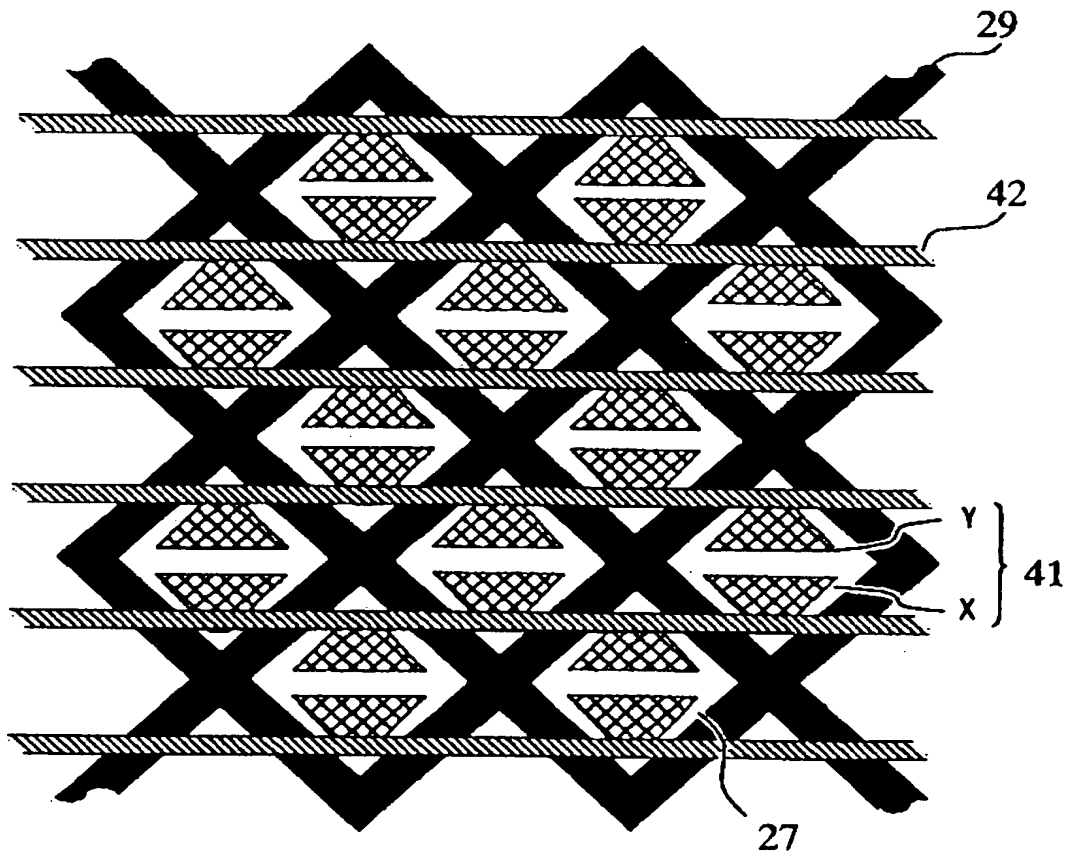
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FIG. 12

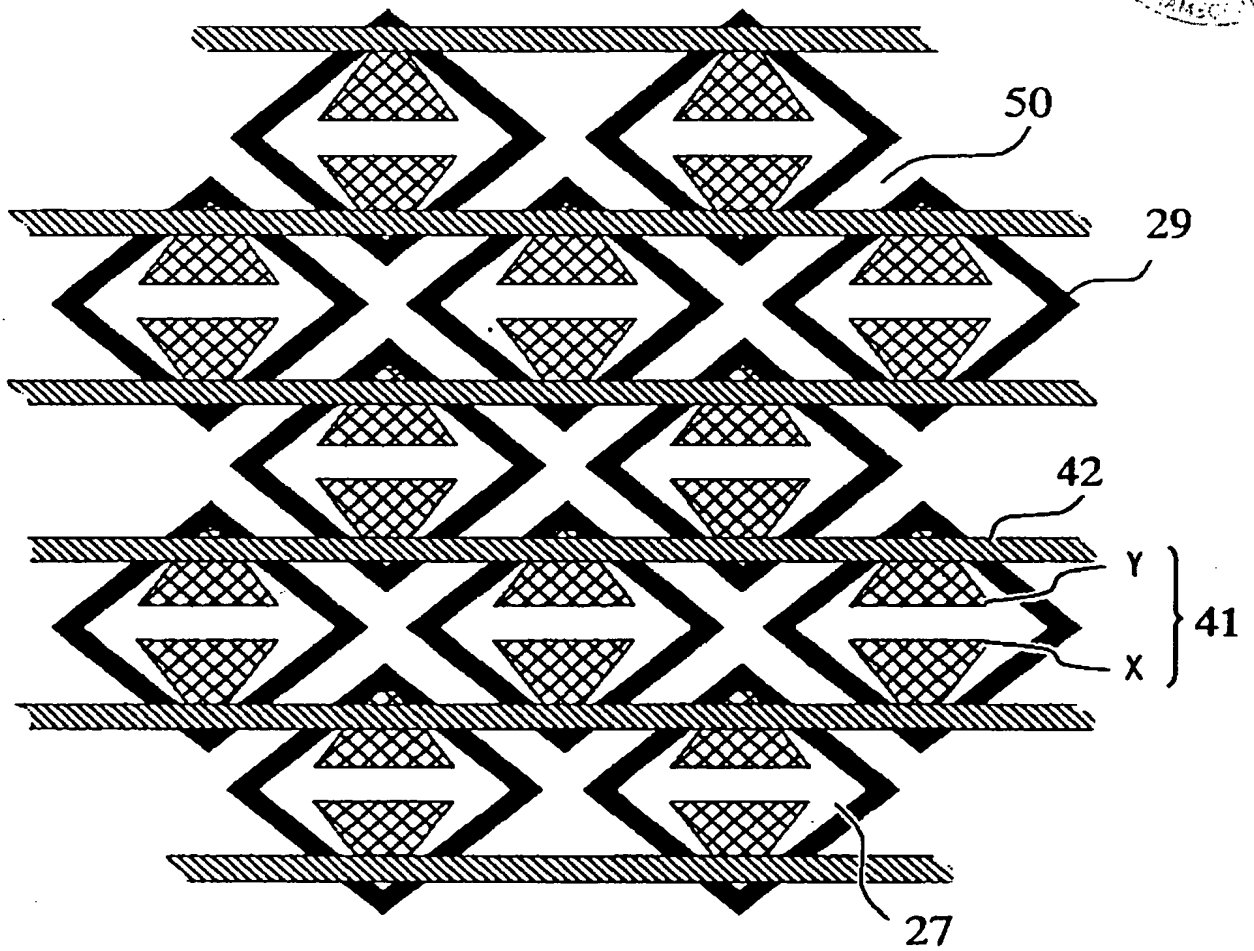


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**FIG. 13**



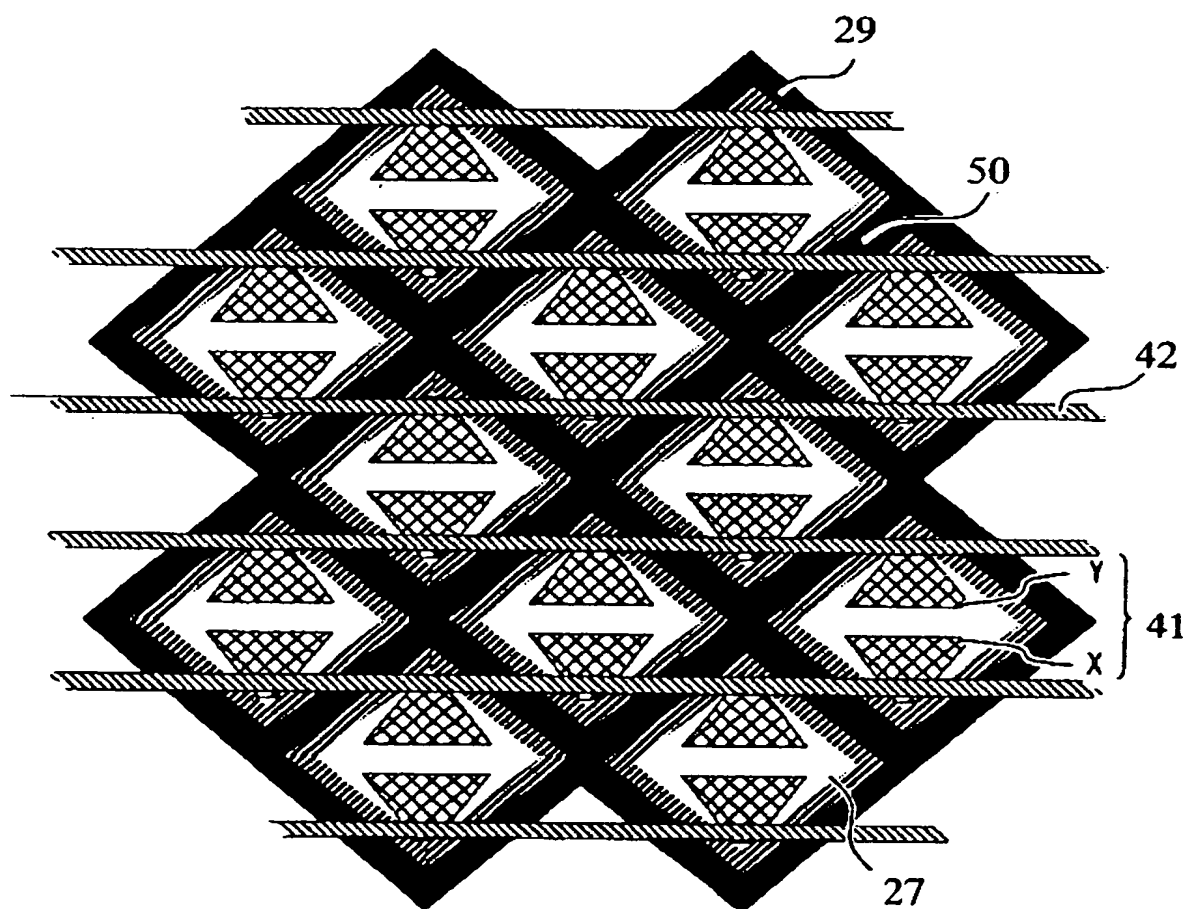
**FIG. 14**



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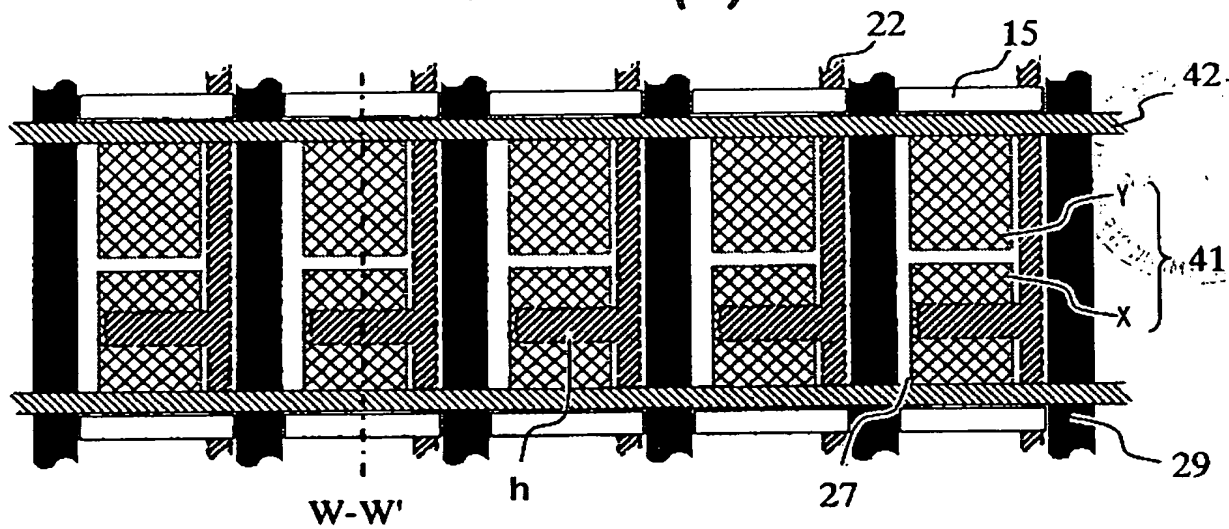
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FIG. 15

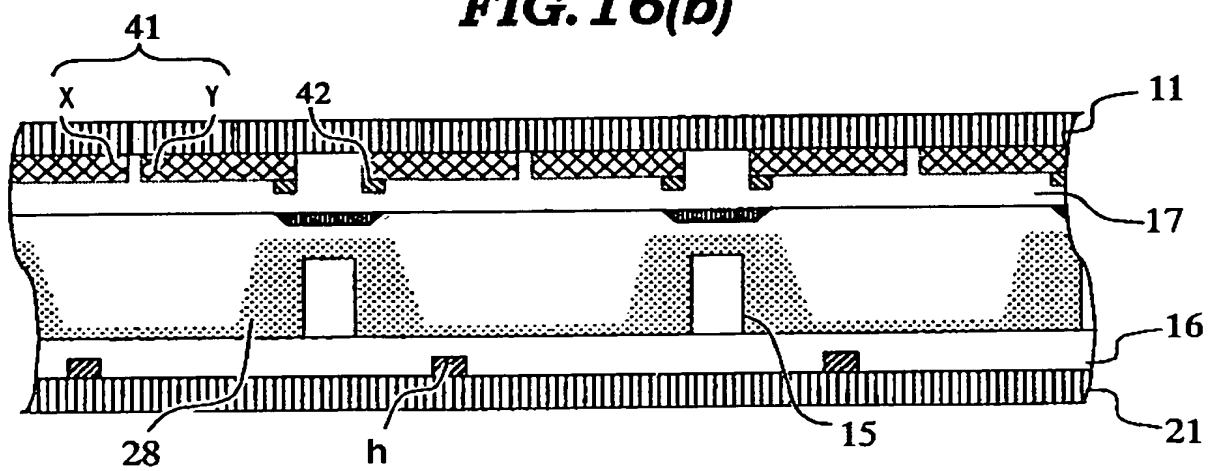


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**FIG. 16(a)**



**FIG. 16(b)**



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FIG. 17(a)

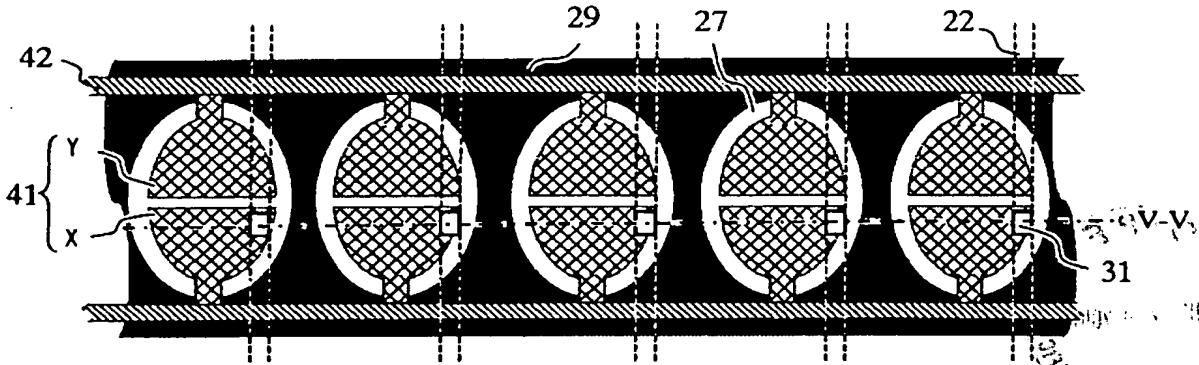


FIG. 17(b)

